



0172

GOVERNMENT OF KARNATAKA

KARNATAKA STATE PRE-UNIVERSITY EDUCATION EXAMINATION BOARD

II YEAR PUC EXAMINATION 2017

SCHEME OF VALUATION

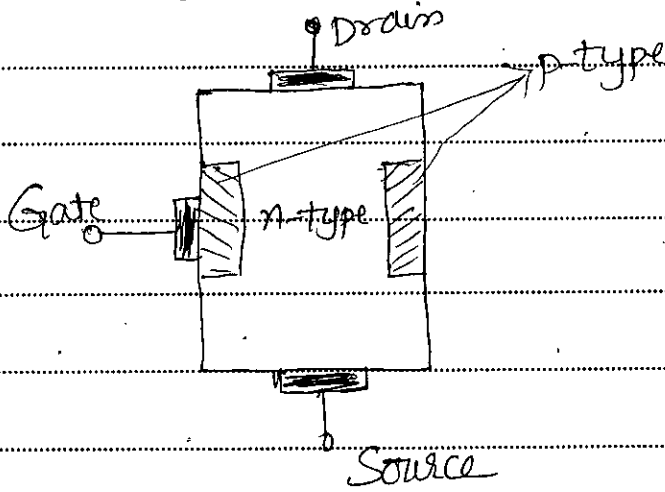
Subject Code : 40

Subject : ELECTRONICS

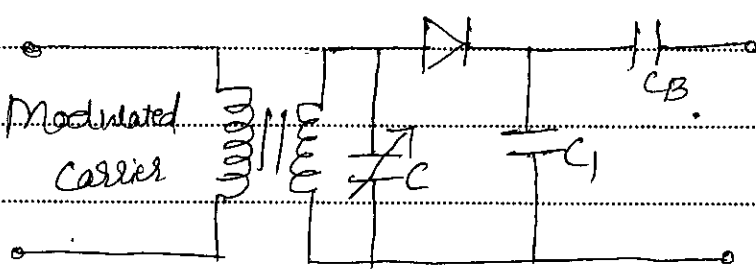
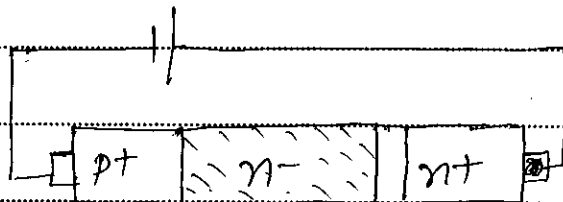
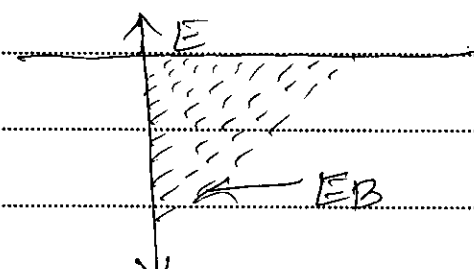
Qn. No.	PART-A	(NS) Marks
1.	It is defined as the ratio of small change in $V_{DS}$ to the corresponding change in $V_{GS}$ for a constant drain current $I_D$ .	①
2.	It is the ratio of differential mode gain to the common mode gain or $CMRR = \frac{A_d}{A_c}$	①
3.	$3 \times 10^8 \text{ m/s}$	①
4.	455 kHz	①
5.	$M.I = \frac{\Delta f}{f_m} = \frac{12 \times 10^3}{6 \times 10^3} = 2$	①
6.	Drift layer or Epitaxial layer or $\bar{n}$	①
7.	An octet is a group of eight adjacent 1's in K-map.	①
8.	$\begin{array}{cc} \begin{array}{c} 6 \\ +3 \\ \hline (1001) \end{array} & \begin{array}{c} 9 \\ +3 \\ \hline (1100) \end{array} & (69)_{10} = (1001 \ 1100)_{\text{Ex-3}} \end{array}$	①
9.	Because as it can process only 8-bits data at a time.	①
10.	2 bytes.	①

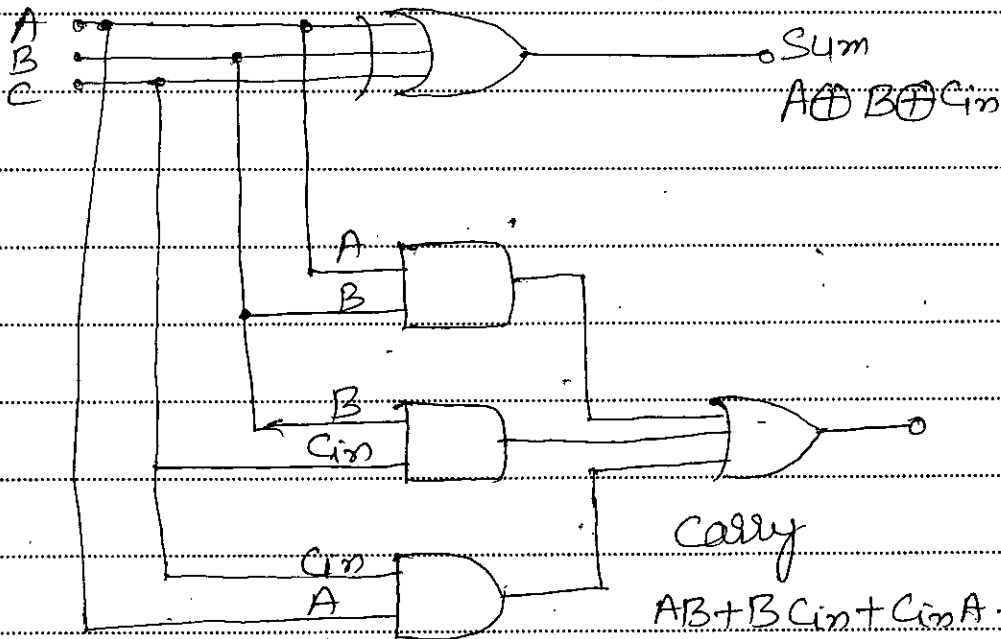
5710

Qn. No.	PART-B	Marks
11.	1) Reducing all the ac sources to zero 2) opening all the capacitors	①
12.	$A_f = \frac{A}{1 + AB}$ $B = 0.008 \text{ or } 0.8\%$	①
13.	<p>Sinusoidal oscillator produce an output having Sine wave Ex: LC oscillator, RC oscillator, Crystal oscillator</p> <p>Non-Sinusoidal oscillator produce an output having traingular, square, <sup>xts</sup>Sawtooth etc wave form Ex: Multivibrator, <sup>relax</sup>Relaxation oscillator</p>	①
14.	1) AC to DC Rectifier 2) AC to AC Voltage Controller 3) DC to DC chopper 4) DC to AC inverter	②
15.	$V_{AK} = \hat{V}_i + V_{RD}$ $= V_i + R_{ON} I_F$ $= 0.4 + 0.002 \times 75 = 0.55V$	①
16.	<p>Accumulator:- Accumulator is an 8-bit Register in 8051 microcontroller. Accumulator is used by all arithmetic &amp; logical instructions. In accumulator one of the operands is stored in it before the execution of an instruction and it also stores the result after the execution of an instruction.</p>	①

Qn. No.		Marks
	<p>program Counter :- It holds the address of a byte in memory. It also specifies the address of the next instruction to be fetched and executed.</p>	<p>①</p>
17.	<p>(i) = = → Equal to</p>	<p>①</p>
	<p>(ii) &amp; &amp; → Logical AND</p>	<p>①</p>
18.	<p><del>Explain any two applications of radars.</del></p>	
	<p>1) Radars are used in defence weapon systems                  2) Safety navigational applications                  3) Location of enemy missiles, planes &amp; ships.                  4) Landing of planes in bad weather conditions                  5) Used as altimeters to measure height.                  6) Special terrain - following radars allow high speed jets to fly very close to the ground to avoid detection by the enemy radars.</p>	
	<p>(write any 2 applications)</p>	<p>2</p>
	<p><u>PART-C</u></p>	
19.		<p>①</p>
	<p>Explanation</p>	<p>②</p>

Qn. No.		Marks
20.	<p><u>Thermal Runaway</u> :- Self destruction of a transistor due to increase in temperature and hence leakage current is called thermal runaway.</p>	①
	<p><u>Leakage current</u> :- The flow of current through the device due to the motion of minority charge carriers under reverse bias condition is called leakage current.</p>	①
	<p><u>Heat Sink</u> :- The device which absorbs unnecessary heat generated in the working device and radiates to the surroundings &amp; safeguards the working device is called heat sink.</p>	①
21	For Block diagram	①
	Derivation (Steps)	①
	$Z_{if} = Z_i (1 + AB)$	①
22.	<p><u>Critical angle</u> :- It is the maximum angle of incidence of a Radio wave under Ionospheric propagation that gets returned to the earth</p>	①
	<p><u>Critical frequency</u> :- It is the highest frequency of a beamed radio wave that will be returned to the earth.</p>	①
	<p><u>Skip distance</u> :- It is the shortest or minimum distance measured between the transmitting antenna &amp; the first receiving antenna measured along the surface of the earth after ionospheric propagation</p>	①

Qn. No.		Marks
23.	<p>The process of recovering a modulating or baseband signal from the modulated wave is known as demodulation or detection</p> 	<p>(1)</p> <p>(2)</p>
24.	<p>In "non-punchthrough" diodes the depletion layer boundary doesn't reach the end of the drift layer. The electric field strength is maximum at the p<sup>+</sup>n<sup>-</sup> and decrease to zero at the end of the depletion region.</p>	<p>(1)</p>
Fig(a)		<p>(1)</p>
Fig(b)		<p>(1)</p>
	<p>Fig(a) Non-punch through in reverse biased power diode :</p>	
	<p>(b) Electric field strength in non-punchthrough power diode.</p>	

Qn. No.		Marks
25.	 <p>Sum <math>A \oplus B \oplus C_{in}</math></p> <p>Carry <math>AB + BC_{in} + C_{in}A</math></p>	<p>(1)</p>
	<p>Sum = <math>A \oplus B \oplus C_{in}</math></p>	<p>(1)</p>
	<p>Carry = <math>AB + BC_{in} + C_{in}A</math></p>	<p>(1)</p>
26.	<p>Additional features of 3G and 4G cell phone systems are</p> <ol style="list-style-type: none"> <li>1) colour LCD screens.</li> <li>2) Digital cameras</li> <li>3) E-mail, Games.</li> <li>4) GPS, Bluetooth</li> <li>5) Internet access &amp; 6) Video Conferencing.</li> </ol> <p>(Any 3)</p>	<p>(3)</p>

Qn. No.	PART-D	Marks
27.	$V_{10K\Omega} = V_2 = \frac{V_{CC} \times R_2}{R_1 + R_2}$ $= \frac{15 \times 10 \times 10^3}{100 \times 10^3 + 10 \times 10^3}$ $= \frac{15 \times 10^3 \times 10}{10^3 (100 + 10)} = \frac{150}{110} = 1.36V$ $I_E = \frac{V_2 - V_{BE}}{R_E}$ $= \frac{1.36 - 0.32}{220} = 0.00454A$ $= 4.54mA$ $r_{e'} = \frac{52mV}{I_E} = \frac{52 \times 10^{-3}}{4.54 \times 10^{-3}} = 11.45\Omega$ $A_v = \frac{R_C}{r_{e'}} = \frac{2.2 \times 10^3}{11.45} = \frac{2200}{11.45} = 192$	<p>①</p> <p>①</p> <p>①</p> <p>①</p> <p>①</p> <p>①</p>
28.	<p>For the first op-amp acting as an inverting amplifier</p> $A = \frac{V_o}{V_{in}} = -\frac{R_f}{R_{in}}$ $\frac{V_{o1}}{300 \times 10^3} = -\frac{500 \times 10^3}{250 \times 10^3}$ $V_{o1} = -600 \times 10^{-3}V$	<p>①</p> <p>①</p>

Qn. No.		Marks
	<p>For the second op-amp acting as non-inverting op-amp adder.</p> $V_0 = -R_f \left[ \frac{V_1}{R_1} + \frac{V_2}{R_2} \right]$ $= -R_f \left[ \frac{V_{01}}{R_1} + \frac{V_2}{R_2} \right]$ $= -500 \times 10^3 \left[ \frac{-600 \times 10^{-3}}{100 \times 10^3} + \frac{700 \times 10^{-3}}{50 \times 10^3} \right]$ $= -500 \times 10^3 \times 10^{-3} \left[ \frac{-600}{100} + \frac{700}{50} \right]$ $= -500 \times 10^{-3} [-6 + 14]$ $= -500 \times 10^{-3} \times 8$ $= -4000 \times 10^{-3} \text{ V}$ $= -4 \text{ V}$	<p>①</p> <p>①</p> <p>①</p>
29.	<p>Given <math>f = 1.13 \text{ MHz}</math></p> $= 1.13 \times 10^6 \text{ Hz}$ $L = 20 \mu\text{H}$ $= 20 \times 10^{-6} \text{ H}$ $C_1 = 0.1 \mu\text{F}$ $= 0.1 \times 10^{-6} \text{ F}$ $f = \frac{1}{2\pi\sqrt{LC_s}}$ $\phi_2 = \frac{1}{4\pi^2 LC_s}$	<p>①</p>



Qn. No.		Marks
	$C_s = \frac{1}{4\pi^2 f^2 L}$	
	$= 0.00099 \mu\text{F}$	③
	$C_s = \frac{C_1 C_2}{C_1 + C_2}$	
	$C_2 = 0.001 \mu\text{F}$	①
30.	<p>Given <math>V_c = 80\text{V}</math>, <math>V_{m} = 32\text{V}</math>.</p>	
	<p>Modulation index <math>m = \frac{V_m}{V_c}</math></p>	
	<p>Modulation Index <math>m = \frac{32}{80}</math></p>	
	<p><math>m = 0.4</math></p>	①
	<p>Amplitude of each sideband</p>	
	$= \frac{m V_c}{2} = \frac{0.4 \times 80}{2} = 16\text{V}$	①
	<p>AM Wave equation = <math>V_c(1 + m \sin \omega_m t) \sin \omega_c t</math></p>	
	$V_{AM} = 80(1 + 0.4 \sin 2\pi \times 10^3 t) \sin 2\pi \times 10^5 t$	
	$f_m = \frac{\omega_m}{2\pi} = \frac{2\pi \times 10^3}{2\pi} = 1\text{KHz}$	①
	$f_c = \frac{\omega_c}{2\pi} = 100\text{KHz}$	①
	<p>Lower sideband frequency = 99 KHz</p>	
	<p>Upper sideband frequency = 101 KHz</p>	
	<p>Frequency Spectrum of AM wave</p>	
	<p style="text-align: center;"><math>V_c \uparrow 80\text{V}</math></p> <p style="text-align: center;"><math>\frac{m V_c}{2} \uparrow 16\text{V}</math>      <math>16\text{V} \uparrow \frac{m V_c}{2}</math></p> <p style="text-align: center;"><math>99\text{KHz}</math>      <math>101\text{KHz}</math></p> <p style="text-align: center;"><math>f_c - f_m</math>      <math>f_c + f_m</math></p> <p style="text-align: center;"><math>\uparrow \text{LSB}</math>      <math>\uparrow \text{USB}</math></p>	①

Qn. No.		Marks																									
31.	<div style="text-align: right; margin-right: 20px;">→ <math>\overline{BD}</math></div> <table border="1" style="border-collapse: collapse; text-align: center;"> <tr> <td style="padding: 5px;"><math>AB \setminus CD</math></td> <td style="padding: 5px;"><math>\overline{C}\overline{D}</math></td> <td style="padding: 5px;"><math>\overline{C}D</math></td> <td style="padding: 5px;"><math>CD</math></td> <td style="padding: 5px;"><math>C\overline{D}</math></td> </tr> <tr> <td style="padding: 5px;"><math>\overline{A}\overline{B}</math></td> <td style="padding: 5px;">0</td> <td style="padding: 5px;">1</td> <td style="padding: 5px;">1</td> <td style="padding: 5px;">0</td> </tr> <tr> <td style="padding: 5px;"><math>\overline{A}B</math></td> <td style="padding: 5px;">1</td> <td style="padding: 5px;">0</td> <td style="padding: 5px;">0</td> <td style="padding: 5px;">1</td> </tr> <tr> <td style="padding: 5px;"><math>AB</math></td> <td style="padding: 5px;">X</td> <td style="padding: 5px;">0</td> <td style="padding: 5px;">0</td> <td style="padding: 5px;">X</td> </tr> <tr> <td style="padding: 5px;"><math>A\overline{B}</math></td> <td style="padding: 5px;">0</td> <td style="padding: 5px;">1</td> <td style="padding: 5px;">X</td> <td style="padding: 5px;">0</td> </tr> </table> <p style="margin-top: 10px;">Entering K-map Looping</p>	$AB \setminus CD$	$\overline{C}\overline{D}$	$\overline{C}D$	$CD$	$C\overline{D}$	$\overline{A}\overline{B}$	0	1	1	0	$\overline{A}B$	1	0	0	1	$AB$	X	0	0	X	$A\overline{B}$	0	1	X	0	<p>②</p> <p>①</p>
$AB \setminus CD$	$\overline{C}\overline{D}$	$\overline{C}D$	$CD$	$C\overline{D}$																							
$\overline{A}\overline{B}$	0	1	1	0																							
$\overline{A}B$	1	0	0	1																							
$AB$	X	0	0	X																							
$A\overline{B}$	0	1	X	0																							
	$Y = \overline{B}D + B\overline{D} \Rightarrow \text{XOR Gate}$	<p>①+①</p>																									
32.	<p>Direct coupled amplifier is an amplifier in which the output of one stage (collector) is connected directly to the input (base) of the next amplifier stage.</p> <p>• circuit diagram</p> <p>Working</p> <p>Frequency response curve</p>	<p>①</p> <p>①</p> <p>②</p> <p>①</p>																									
33.	<p>An amplifier whose output voltage is proportional to difference of the input voltages is called difference amplifier or Subtractor</p> <p>circuit diagram</p> <p>Derivation</p> <p>Final Expression</p>	<p>①</p> <p>①</p> <p>②</p> <p>①</p>																									

Qn. No.		Marks
34.	Block diagram Working	③ ②
35.	Logic circuit Working Truth-table Timing diagram	① ② ① ①
36.	MOV A, #1FH MOV R0, #B4H ADD A, R0 MOV R7, A Data: 1FH = 0001 1111 + B4H = 1011 0100 Sol <sup>n</sup> : = 1101 0011 = D3H	① ① ① ① ①
37.	The process of identifying and correcting the errors in the program is called as debugging. Naming the errors. Explanation of different types of error "XXX"	①+① ③

